

I CLAIM

1. A computer system, comprising:

a processor generating data output;

a hard disk drive adapter forming an intermediate adapter connected to receive the data output from said processor and configured to distribute the data output to a plurality of channels; and

a plurality of data storage devices each connected to said hard disk drive adapter via a respective serial ATA cable and each receiving the data output of a respective channel.

2. The computer system according to claim 1, wherein said intermediate adapter is configured to mirror or split the data output to the plurality of channels.

3. The computer system according to claim 1, wherein said processor is a central processor with a serial ATA connector communicating with said intermediate adapter via a serial ATA cable.

4. The computer system according to claim 1, wherein said processor is configured to generate the data in serial ATA format and said intermediate adapter is configured to communicate in the serial ATA standard.

5. A hard disk drive adapter configured to form an intermediate adapter, comprising a serial ATA interface for communicating with a central processor, a plurality of

serial ATA interfaces each for communicating with a respective serial ATA storage device, and means for mirroring, or splitting, a data input from the central processor to a plurality of channels each assigned to a respective one of the serial ATA interfaces for communicating with the serial ATA storage devices.

6. The intermediate adapter according to claim 5 configured to implement substantially automatic, substantially instantaneous RAID 1 hard disk drive mirroring, or other disk data transfer, with serial ATA.

7. In a computer system having a processor generating data output and at least one data storage device for receiving and storing the data output, the improvement which comprises:

a plurality of serial ATA data storage devices;

an intermediate adapter connected between the processor and the data storage devices for receiving the data output from the processor and for outputting the data to the data storage devices; and

means connected in said intermediate adapter for distributing the data output to a plurality of channels;

a plurality of serial ATA connections connecting each of the channels to a respective one of said serial ATA data storage device.

8. The computer system according to claim 7, wherein said serial ATA connections are serial ATA cables connecting the channels of said intermediate adapter to the data storage devices.

9. The computer system according to claim 7, wherein said intermediate adapter is entirely transparent to the computer system.